

FIG 1A

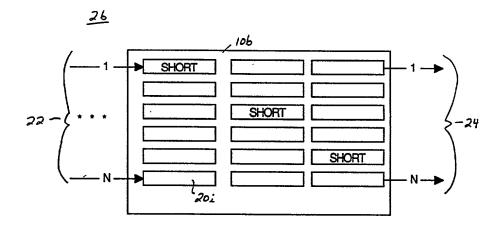


FIG 1B

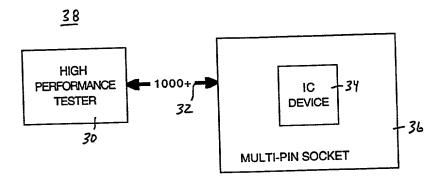


FIG. 1C

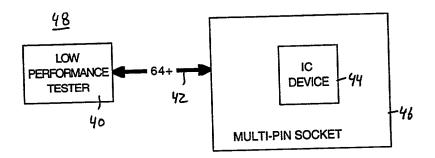


FIG. 1D

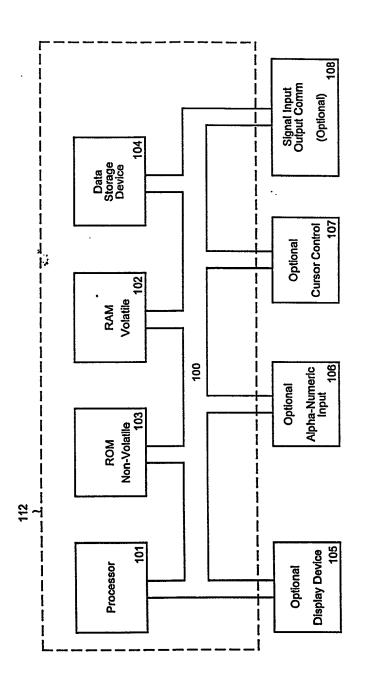


FIG. 2

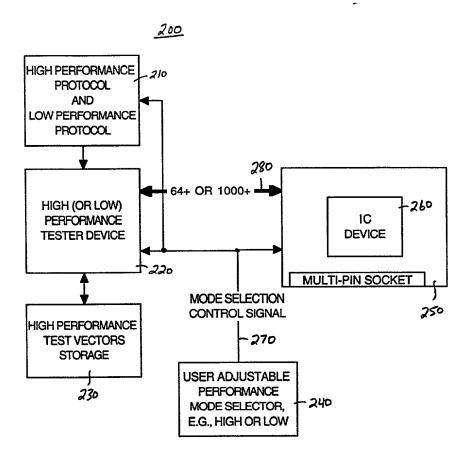
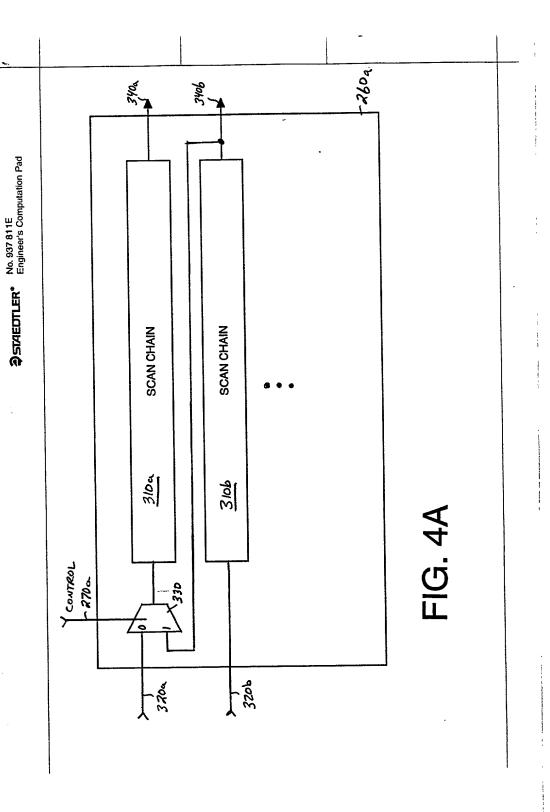


FIG. 3



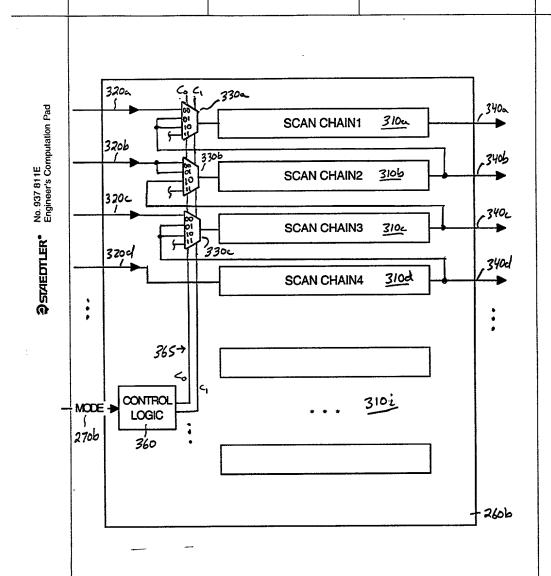


FIG. 4B

STAEDTLER*

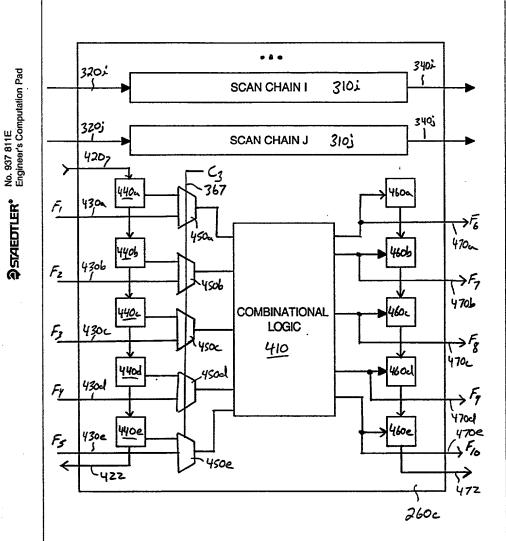
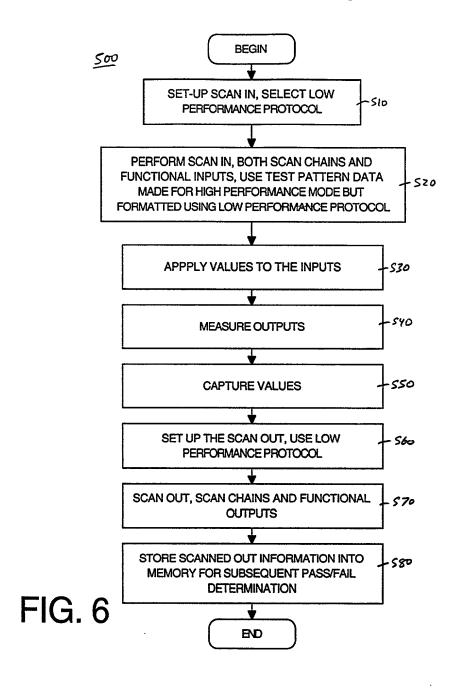


FIG. 5



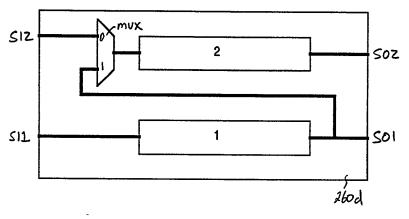


FIG. 7A

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Shift { V { sil = 'sil si2'; so2 = 'so2 sol'; \mathscr{L} = 660

funcln = 'func_inputs'; funcOut = 'func_outputs_p'clk = P; } \mathcal{L} = 670

C { scan_enable = 0; }
                           If ( reconfig_signal == few_inputs )
                                                                                                                                                                                                                         Vfclk = P; I - 680
                 Do_one_test {
MacroDefs (
                                                                                                  7019
```

FIG. 7B

STAEDTLER* No. 937 811E Engineer's Computation Pad